

Name: _____

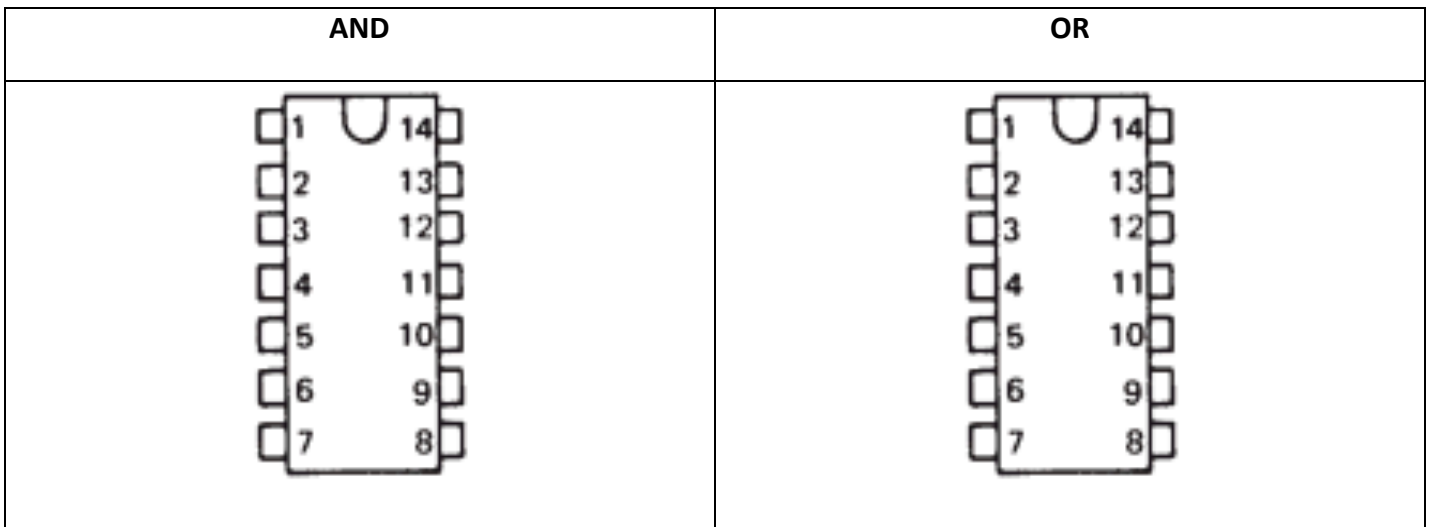
Lab Section: _____

ECE 2140

Experiment 3 Prelab

AND & OR Gates

1. Read through Experiment 3.
2. Look up the data sheets for the AND and OR gate ICs.
 - a. Label all pins according to the data sheet:



- b. Find the following specifications for each IC:

Absolute Maximum Voltages		
	AND	OR
V _{cc}		
Input voltage		

Recommended (Nominal) Operating Voltages		
	AND	OR
V_{CC}		
V_{IL}		
V_{IH}		
V_{OL}		
V_{OH}		

3. Draw a schematic for each part of the Experiment:

a. Part 3:

b. Part 4:

c. Part 5: