

## **Tutorials on Verilog**

<http://www.asic-world.com/verilog/veritut.html>

<http://www.cpu-turkey.com/upload/group/4/VerilogPresent-v1.3.pdf>

<http://www.veriligtutorial.info/>

## **Tutorials on Quartus**

[ftp://ftp.altera.com/up/pub/Tutorials/DE2/Digital\\_Logic/tut\\_simulation\\_verilog.pdf](ftp://ftp.altera.com/up/pub/Tutorials/DE2/Digital_Logic/tut_simulation_verilog.pdf)

<http://www.ece.tufts.edu/~hchang/ee129-f06/project/project2/Tutorial.pdf>

[http://lslwww.epfl.ch/pages/teaching/cours\\_lsl/sl\\_info/Labo5.b.pdf](http://lslwww.epfl.ch/pages/teaching/cours_lsl/sl_info/Labo5.b.pdf)